

STB6100

8PSK/QPSK direct conversion tuner IC

DATA BRIEF

DESCRIPTION

The STB6100 ASIC is a highly integrated, low-cost direct conversion (zero IF) tuner IC for DVB-S2 broadcast satellite applications.

This device includes an LNA, down-converting mixers, baseband low-pass filters, gain control, on-chip VCO and a low noise PLL. Its wide gain control range is well suited for low symbol rate applications.

The STB6100 is controlled by a simple two wire interface and has been designed to minimize the external bill of materials to reduce manufacturing costs and to simplify the board layout. The reduced component count ensures greater system reliability leading to increase consumer satisfaction.

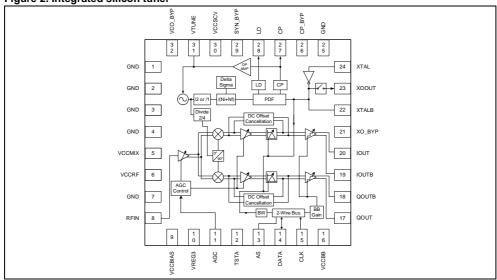
The DVB-S2 standard increases the capacity of satellite broadcasts by as much as 30%, so enabling the effective deployment of high data rate applications such as high definition TV and broadband internet.

Packaged in a small QFN 32-pin package, it is ideal for low cost DBS design. $\label{eq:packaged} % \begin{subarray}{ll} \end{subarray} % \begin$

Figure 1. Package (420 + 36 PBGA 27 x 27)



Figure 2. Integrated silicon tuner



Rev. 1 September 2004 1/2

STB6100

FEATURES

- Powered by RF Magic
- Direct conversion
- Input range 950 to 2150 MHz
- Supports 1 to 45 Msymbol/s
- Fully integrated LO and PLL
- Differential I/Q outputs
- Buffered XO output to clock other devices
- Low external component count
- Low power consumption

- 2-wire I²C serial interface
- Low cost integrated ZIF tuner for DVB-S2 and DIRECTV, VSAT and internet throuth satellite applications
- 8PSK/QPSK support
- Simple interface to ST's satellite demodulator devices
- Low phase noise, excellent linearity and high LO harmonics rejection
- Small QFN-32 pin package

Table 1. Order Codes

Part Number	Description
STB6100	Standard tray version
STB6100T	Tape and real

REVISION HISTORY

Table 2. Revision History

Date	Revision	Description of Changes
September-2004	1	First Issue

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